

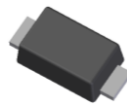
Features

- Ultra-Low Forward Voltage Drop
- Low Leakage Current
- Superior Reverse Avalanche Capability
- Excellent High-Temperature Stability
- Patented Interlocking Clip Design for High Surge Current Capacity
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- +150°C Operating Junction Temperature
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

- Case: POWERDI[®]123
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity Indicator: Cathode Band
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.018 grams (Approximate)

POWERDI[®]123



Top View

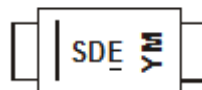
Ordering Information (Note 4)

| Part Number | Case | Packaging |
|--------------|--------------------------|-------------------|
| SBR1A400P1-7 | POWERDI [®] 123 | 3,000/Tape & Reel |

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information

POWERDI[®]123



SDE = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: C = 2015)
 M = Month (ex: 3 = March)

Date Code Key

| Year | 2015 | 2016 | 2017 | 2018 | 2019 | 2020 | 2021 |
|------|------|------|------|------|------|------|------|
| Code | C | D | E | F | G | H | I |

| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
|-------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | O | N | D |

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

| Characteristic | Symbol | Value | Unit |
|---|------------------------------------|-------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V_{RRM} V_{RWM} V_{RM} | 400 | V |
| Average Rectified Output Current (See Figure 1) | I_O | 1.0 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | I_{FSM} | 40 | A |

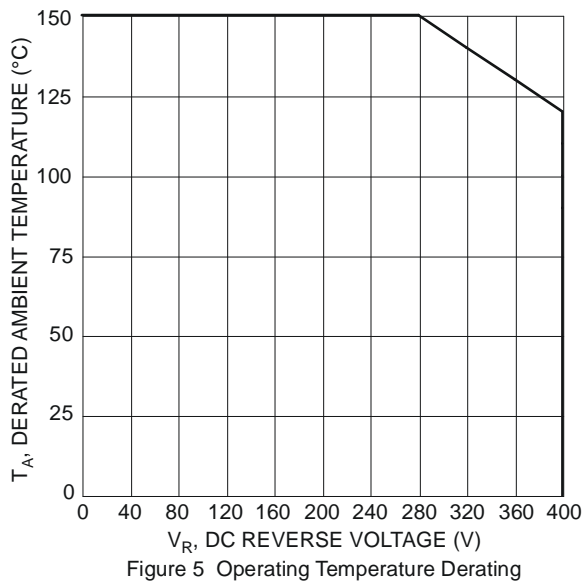
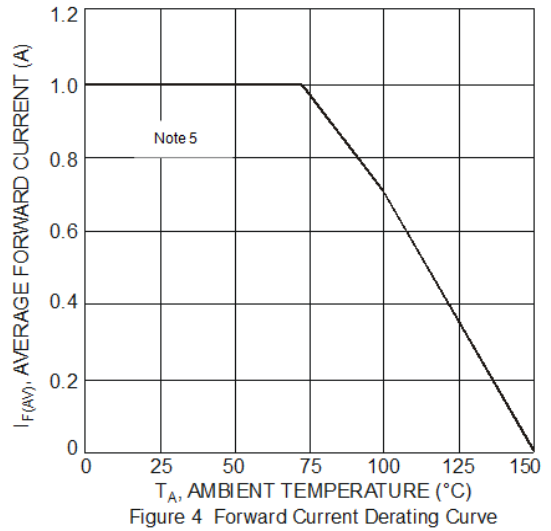
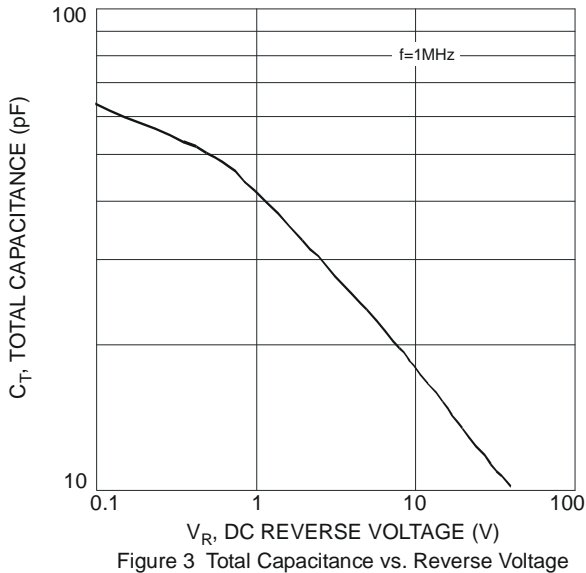
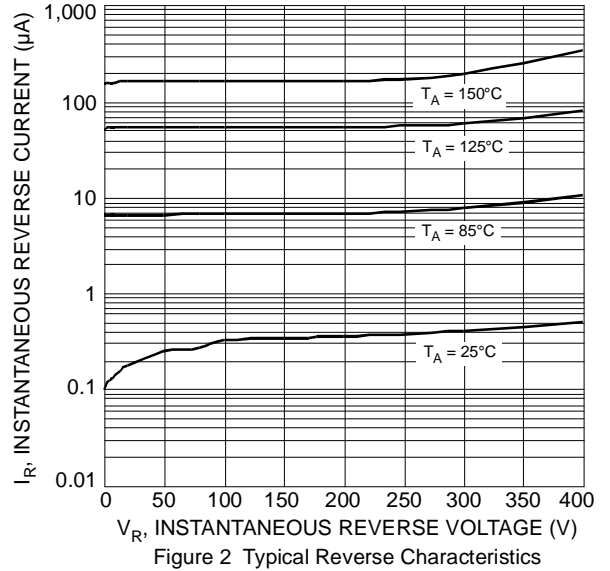
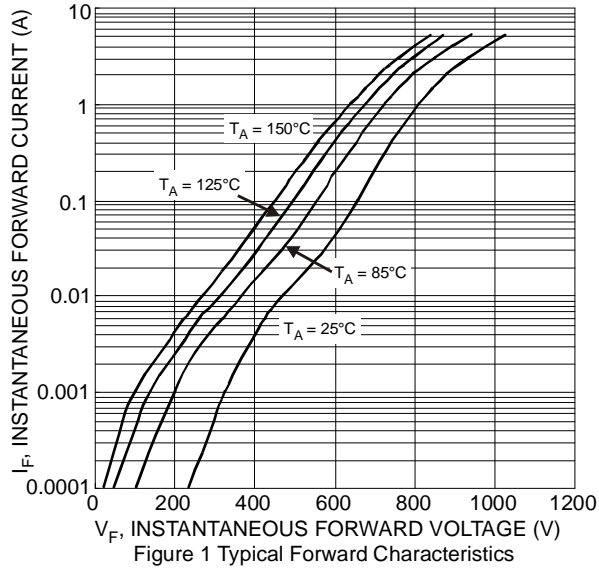
Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|---|-----------------|-------------|--------------------|
| Maximum Thermal Resistance Junction to Ambient (Note 5) | $R_{\theta JA}$ | 138 | $^\circ\text{C/W}$ |
| Operating and Storage Temperature Range | T_J, T_{STG} | -65 to +150 | $^\circ\text{C}$ |

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

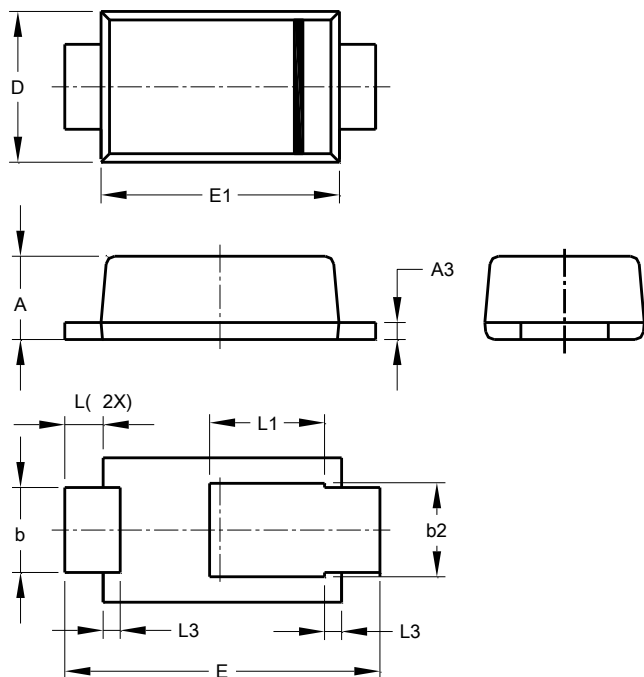
| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|--------------------------|----------|-----|---------------------|-------------------|------|---|
| Forward Voltage | V_F | — | 0.88 — | 1.1 1.0 | V | $I_F = 1.0\text{A}, T_J = +25^\circ\text{C}$ $I_F = 1.0\text{A}, T_J = +125^\circ\text{C}$ |
| Reverse Current (Note 6) | I_R | — | — 0.013 0.073 | 0.05 0.36 2 | mA | $V_R = 400\text{V}, T_J = +25^\circ\text{C}$ $V_R = 400\text{V}, T_J = +85^\circ\text{C}$ $V_R = 400\text{V}, T_J = +125^\circ\text{C}$ |
| Reverse Recovery Time | t_{rr} | — | — | 85 | ns | $I_F = 0.5\text{A}, I_R = 1\text{A},$ $I_{RR} = 0.25\text{A}$ |

Notes: 5. FR-4 substrate PC board, with minimum recommended pad layout per <http://www.diodes.com>.
6. Short duration pulse test used to minimize self-heating effect.



Package Outline Dimensions

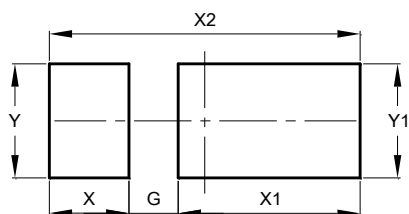
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



| POWERDI [®] 123 | | | |
|--------------------------|-------|-------|------|
| Dim | Min | Max | Typ |
| A | 0.93 | 1.00 | 0.98 |
| A3 | 0.15 | 0.25 | 0.20 |
| b | 0.85 | 1.25 | 1.00 |
| b2 | 1.025 | 1.125 | 1.10 |
| D | 1.63 | 1.93 | 1.78 |
| E | 3.50 | 3.90 | 3.70 |
| E1 | 2.60 | 3.00 | 2.80 |
| L | 0.40 | 0.50 | 0.45 |
| L1 | 1.25 | 1.40 | 1.35 |
| L3 | 0.125 | 0.275 | 0.20 |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| G | 0.65 |
| X | 1.05 |
| X1 | 2.40 |
| X2 | 4.10 |
| Y | 1.50 |
| Y1 | 1.50 |

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